

MCUs

Cortex-M

- [Introductionthe ARM Cortex-M3Exception / Interrupt](#)
- [How to debug a HardFault on an ARM Cortex-M MCU](#)
- Disabling/Enabling interrupts (critical sections)
 - [NVIC: Disabling Interrupts on ARM Cortex-M and the Need for a Memory Barrier Instruction](#)
 - Chapter 10 in book: Cortex M3 Definitive Guide

From:

<https://niziak.spox.org/wiki/> - **niziak.spox.org**

Permanent link:

<https://niziak.spox.org/wiki/programming:mcu>

Last update: **2021/03/16 15:49**

