

The system is coming up, please wait...

U-Boot 1.1.3 (Apr 15 2009 - 11:53:19)

CPU: 8548_E, Version: 2.1, (0x80390021)

Core: E500, Version: 2.2, (0x80210022)

Clock Configuration:

CPU:1199 MHz, CCB: 399 MHz,

DDR: 199 MHz, LBC: 49 MHz

L1: D-cache 32 kB enabled

I-cache 32 kB enabled

Board: Elara CPJr+

CPU Board Revision 255.198 (0xffc6)

PCI2: disabled

I2C: ready

DRAM: Initializing DDRSDRAM

memsize = 400

DDR: 1024 MB

Now running in RAM - U-Boot at: 3fb8f000

trap_init : 0x0

system inventory subsystem initialized

FLASH: 4 MB

L2 cache 512KB: enabled

Parity error on Slot ID in CP misc read reg: val=0

ATA interface setup: io_base=0xf8f00000, port=0x1f0, ctl=0x3f6

PCI:

Skip our host bridge

00	10	1657	0010	0280	1a
00	11	1657	0016	ff00	1a
00	12	1657	0016	ff00	1a
00	13	1657	0016	ff00	1a
00	14	1131	1561	0c03	1a
00	14	1131	1562	0c03	1a
02	00	10b5	8508	0604	1a
02	00	10b5	8508	0604	1a
04	01	10b5	8508	0604	1a
04	02	10b5	8508	0604	1a
04	03	10b5	8508	0604	1a
04	04	10b5	8508	0604	1a
04	01	10b5	8508	0604	1a
04	02	10b5	8508	0604	1a
04	03	10b5	8508	0604	1a
04	04	10b5	8508	0604	1a
02	00	10b5	8508	0604	1a
0c	00	1657	0012	0280	1a
04	01	10b5	8508	0604	1a
04	02	10b5	8508	0604	1a
0e	00	1657	0012	0280	3d
04	03	10b5	8508	0604	1a

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0f 00 1657 0012 0280 3e
04 04 10b5 8508 0604 1a
0e 00 1657 0012 0280 3d
0f 00 1657 0012 0280 3e
10 00 1657 0012 0280 3c
Fixup PCI-e 8508 bus, Anvil BAR ..done
In:    serial
Out:   serial
Err:   serial
Net:
ENET0: PHY is Broadcom BCM5461S 10/100/1000 BaseT PHY (2060c1)

Checking system RAM - press any key to stop test

Checking memory address: 00100000

System RAM test using Default POST RAM Test succeeded.

set_bootstatus: BS_LOAD_OS, platform_idx = 6
Hit ESC to stop autoboot:  0
ATA device vendor SILICONSYSTEMS INC 1GB-3625, product C944312024820918J503,
revision 241-0230
Map file at LBA sector 0xa5bcf
Number of fragments is 0x2
End dest point is 0x00695000.
## Booting image at 00400000 ...
   Image Name:   Linux-2.6.14.2
   Image Type:   PowerPC Linux Multi-File Image (gzip compressed)
   Data Size:    2704863 Bytes =  2.6 MB
   Load Address: 00000000
   Entry Point:  00000000
   Contents:
     Image 0:    1628346 Bytes =  1.6 MB
     Image 1:    1076503 Bytes =  1 MB
   Verifying Checksum ... OK
   Uncompressing Multi-File Image ... OK
## Current stack ends at 0x3FB6DBC0 => set upper limit to 0x00800000
## initrd at 0x0058D908 ... 0x0069461E (len=1076503=0x106D17)
   Loading Ramdisk to 1fef9000, end 1ffffd17 ... OK
   initrd_start = 1fef9000, initrd_end = 1ffffd17
## Transferring control to Linux (at address 00000000) ...
PCI: Cannot allocate resource region 0 of PCI bridge 2
PCI: Cannot allocate resource region 2 of PCI bridge 2
PCI: Cannot allocate resource region 0 of PCI bridge 3
PCI: Cannot allocate resource region 2 of PCI bridge 3
PCI: Cannot allocate resource region 0 of PCI bridge 4
PCI: Cannot allocate resource region 2 of PCI bridge 4
PCI: Cannot allocate resource region 0 of PCI bridge 5
PCI: Cannot allocate resource region 2 of PCI bridge 5
PCI: Cannot allocate resource region 0 of PCI bridge 6
PCI: Cannot allocate resource region 2 of PCI bridge 6
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PCI: Cannot allocate resource region 0 of device 0000:00:00.0
PCI: Cannot allocate resource region 1 of device 0000:00:00.0
PCI: Cannot allocate resource region 0 of device 0001:01:00.0
PCI: Failed to allocate mem resource #1:80000000@0 for 0000:00:00.0
----->fsl_i2c_init(): drivers/i2c/busses/i2c-mpc.c Initialization
entered<3>----->iic_debug_create_proc(): drivers/i2c/busses/i2c-mpc.c
About to create proc dir
Installing Linux 2.6 Kernel
Attempting to find a root file system on hda1...
INIT: version 2.78 booting
Bypassing firmware validation.
INIT: Entering runlevel: 3
read_phy_reg(0x1, 0x4, 0x1e1)
write_phy_reg(0x1, 0x4, 0x1e1)
CPLD INIT DONE
sys_chip_init: class 0 max 24 Done
read_phy_reg(0x1, 0x4, 0x1e1)
write_phy_reg(0x1, 0x4, 0x1e1)
Anvil version 2.2 or higher found
2019/02/11-10:46:28, [HAM-1004], 52,, INFO, DL_8000_0008_M, Processor
rebooted - Reboot
```

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SNMP Research EMANATE/Lite Agent Version 16.2.0.9
Copyright 1989-2006 SNMP Research, Inc.
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Fabric OS (DL_8000_0008_M)
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DL_8000_0008_M console login: sysctrld: all services Standby
sec0: Security is initializing.....
sysctrld: all services Active
DCE Blade: Elara board version 7 anvil_allports 1
DCE Blade: Elara board version 7 anvil_allports 1
POST1: Started running Mon Feb 11 10:46:41 GMT 2019
POST1: Test #1 - Running ceeturboramtest
Trying to free free IRQ61
Trying to free free IRQ62
Trying to free free IRQ60
Trying to free free IRQ61
Trying to free free IRQ62
Trying to free free IRQ60
Trying to free free IRQ61
Trying to free free IRQ62
Trying to free free IRQ60
POST1: Script PASSED with exit status of 0 Mon Feb 11 10:46:43 GMT 2019 took
(0:0:2)
POST2: Started running Mon Feb 11 10:46:43 GMT 2019
POST2: Test #1 - Running ceeportloopbacktest (SERDES)
Trying to free free IRQ61
Trying to free free IRQ62
```

```
Trying to free free IRQ60
Trying to free free IRQ61
Trying to free free IRQ62
Trying to free free IRQ60
2019/02/11-10:46:44, [EM-1034], 53,, ERROR, DL_8000_0008_M, PS 1 set to
faulty, rc=2000e.
```

Fabric OS (DL_8000_0008_M)

DL_8000_0008_M console login:

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Last update: **2021/12/13 13:55**

